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February 3, 2007

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Patent No. : 6,834,262 B1
Inventor(s) : Artur Balasinski, et al.
Issued : December 21, 2004
For **A SCHEME FOR IMPROVING THE
SIMULATION ACCURACY OF INTEGRATED
CIRCUIT PATTERNS BY SIMULATION OF
THE MASK.**

Doc. No. **10200/82**

To Whom It May Concern:

The Certificate of Correction issued on January 30, 2007, issued in error, in that error(s) was made in identifying the patent number and/or keying text/corrections, i.e.:

The issue date in the heading of cofc has been incorrectly entered in the heading of the issued cofc. A superseding cofc will be issued to correct the patent issue date to read --December 21, 2004--.

Therefore, a certificate of correction will be issued to correct (supersede) the Certificate of Correction containing error(s), made during preparation of the Certificate of Correction, as noted above.

No further response is required, from applicants (attorney). However, errors discovered by attorney, other than as noted and described above, should be noted on *a copy* of the Certificate of Correction that was issued in error, accompanied by a signed transmittal letter and submitted directed to this Branch.

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cbn